

Electronic Acknowledgement Receipt

EFS ID:	10818267
Application Number:	10801503
International Application Number:	
Confirmation Number:	1822
Title of Invention:	FRACTIONAL-TYPE PHASE-LOCKED LOOP CIRCUIT WITH COMPENSATION OF PHASE ERRORS
First Named Inventor/Applicant Name:	Guido Gabriele Albasini
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Filer Authorized By:	Kevin Duane Jablonski
Attorney Docket Number:	2110-111-03
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Certificate of Correction	2110-111-03_REQCOC_20110825_AsFiled.pdf	68665 cfe7504e0942f22db95074a632b148141541c236	no	2

Warnings:

Information:

2	Fee Worksheet (SB06)	fee-info.pdf	30300 306b9c23dde1271e01308b715a52f7d5dd89e	no	2
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Warnings:

Information:

Total Files Size (in bytes):			98965
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